

FIG. 1

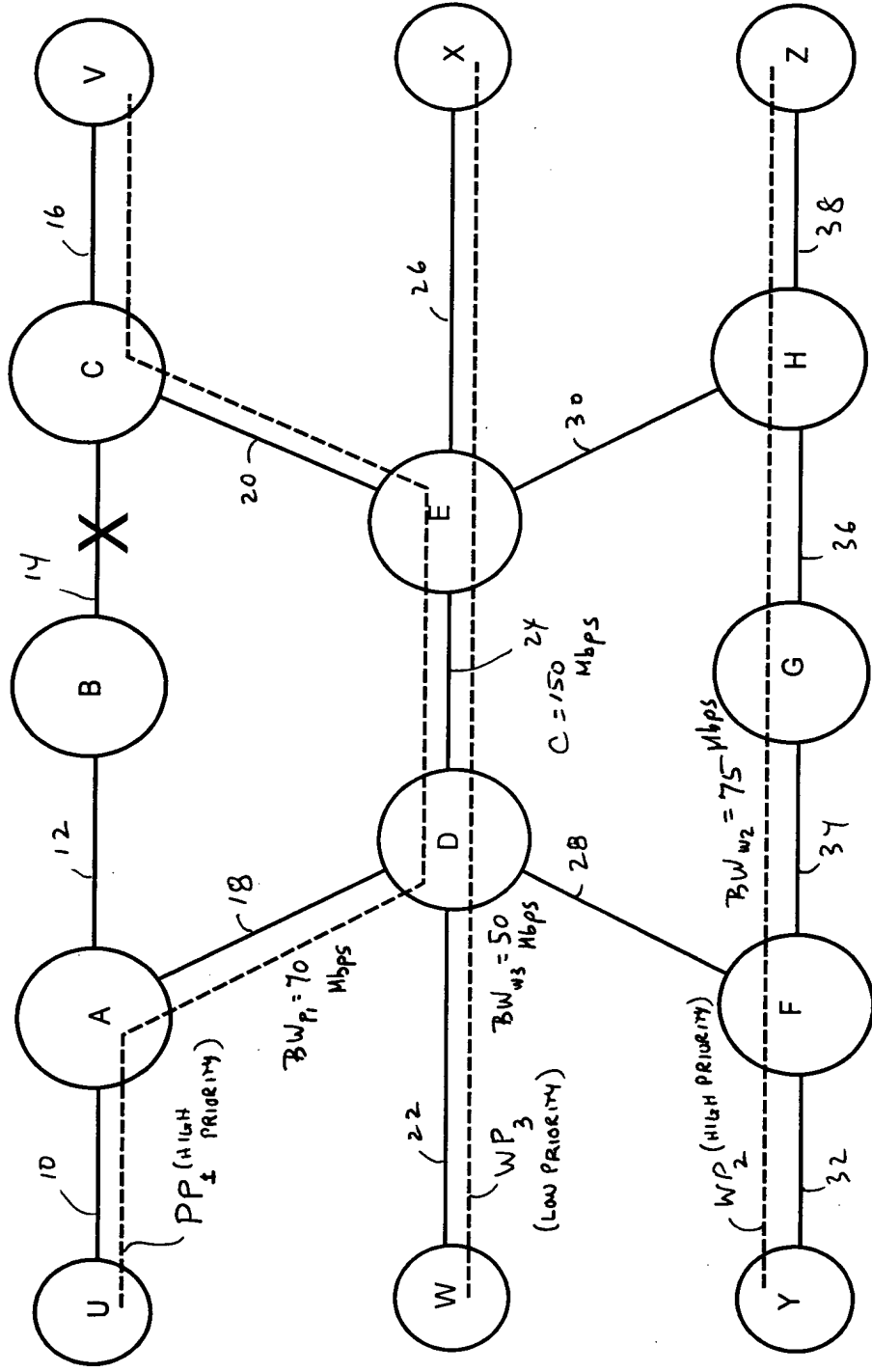


FIG. 2

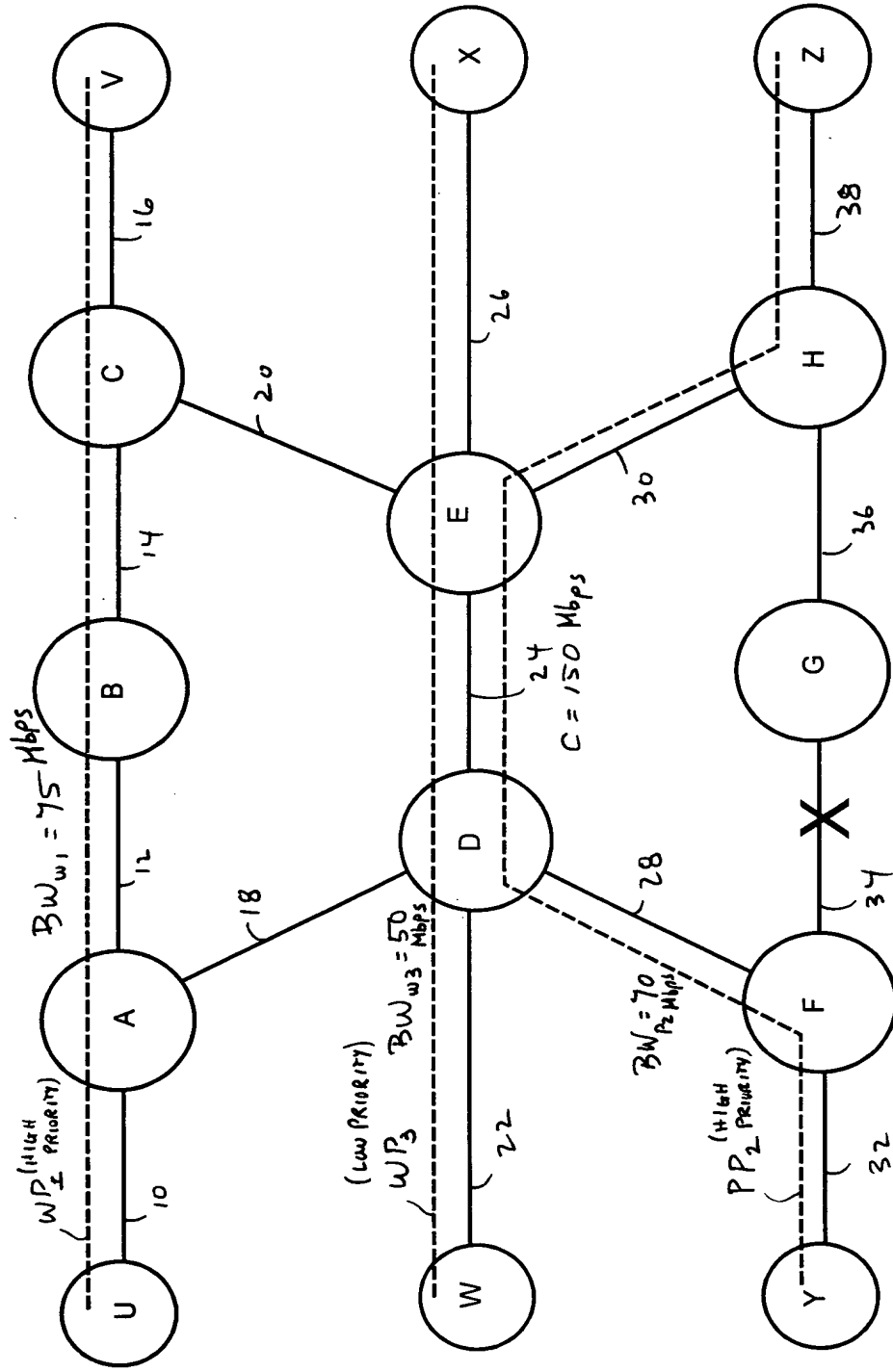


FIG. 3

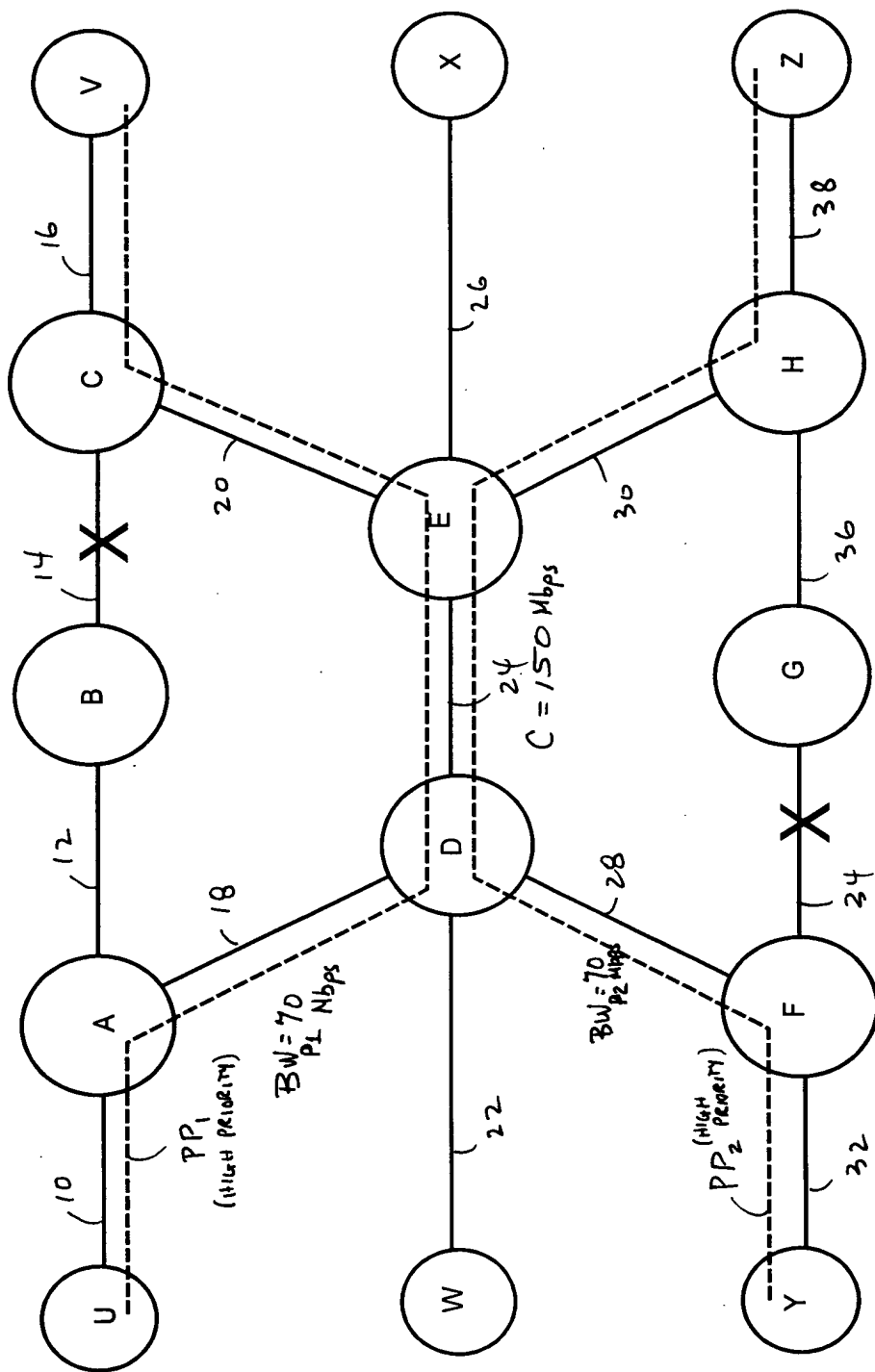


FIG. 4

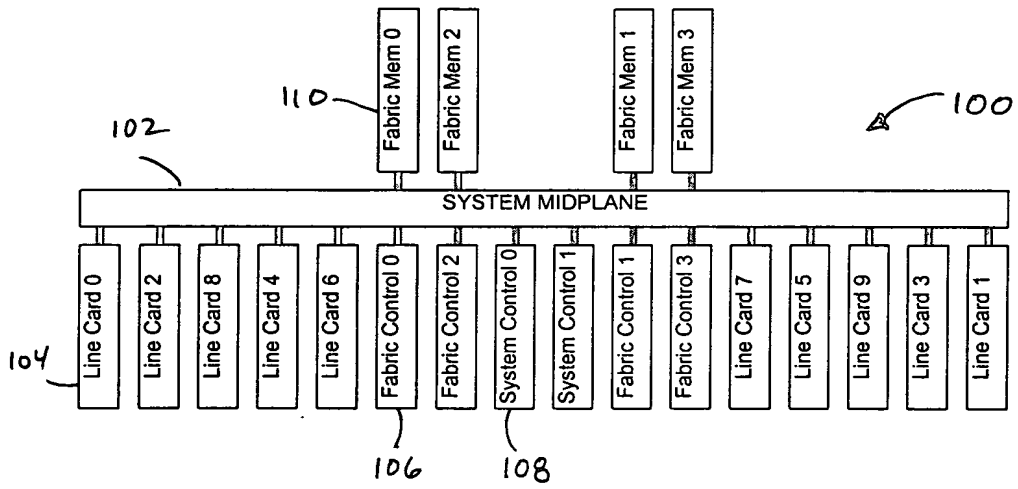


FIG. 5

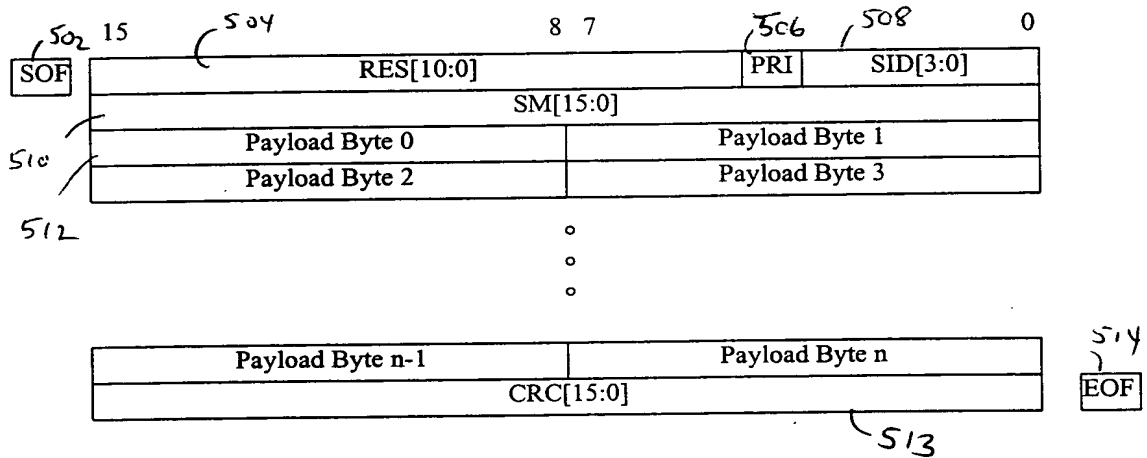


FIG. 6D

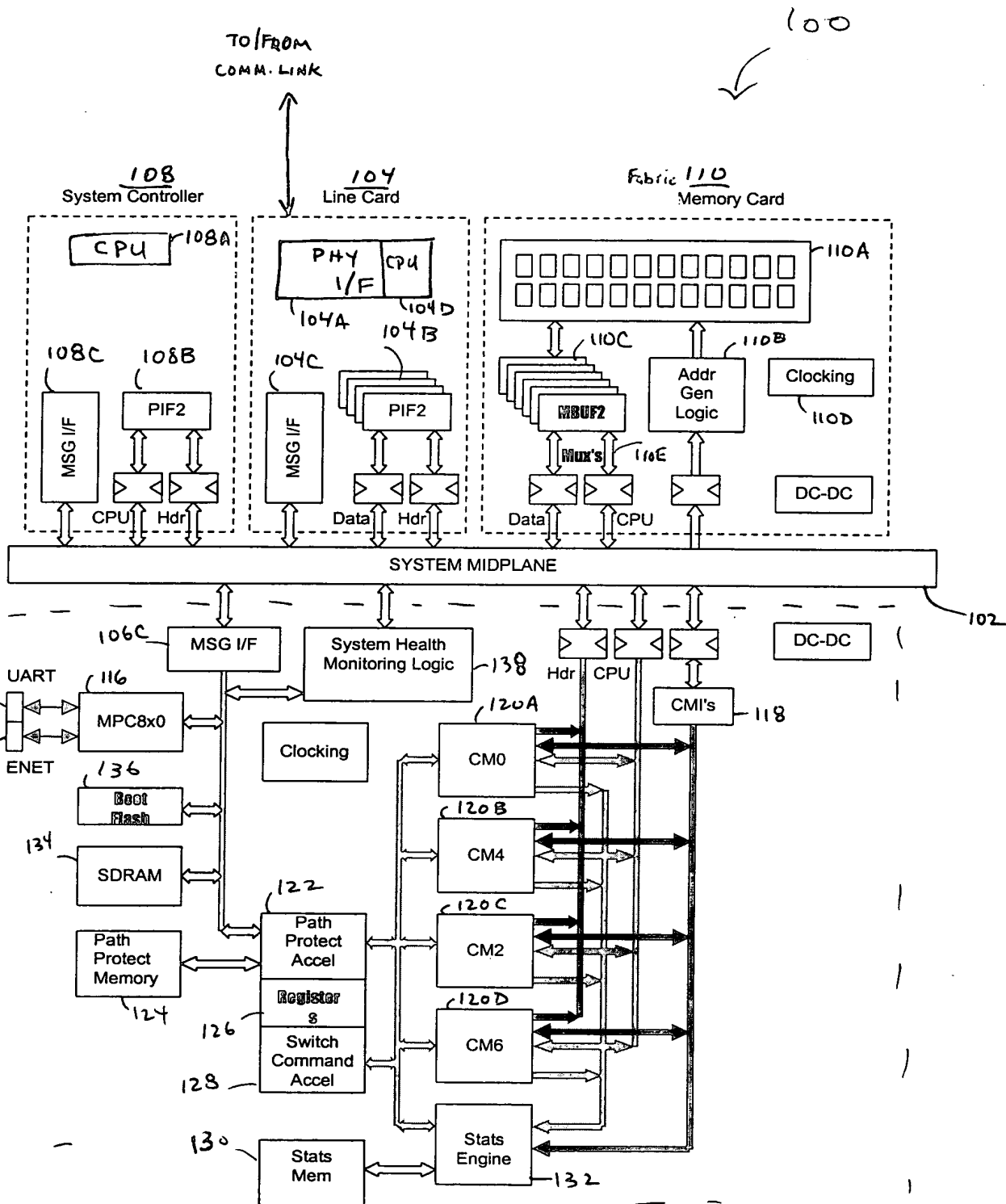
[illegible]

FIG. 6A

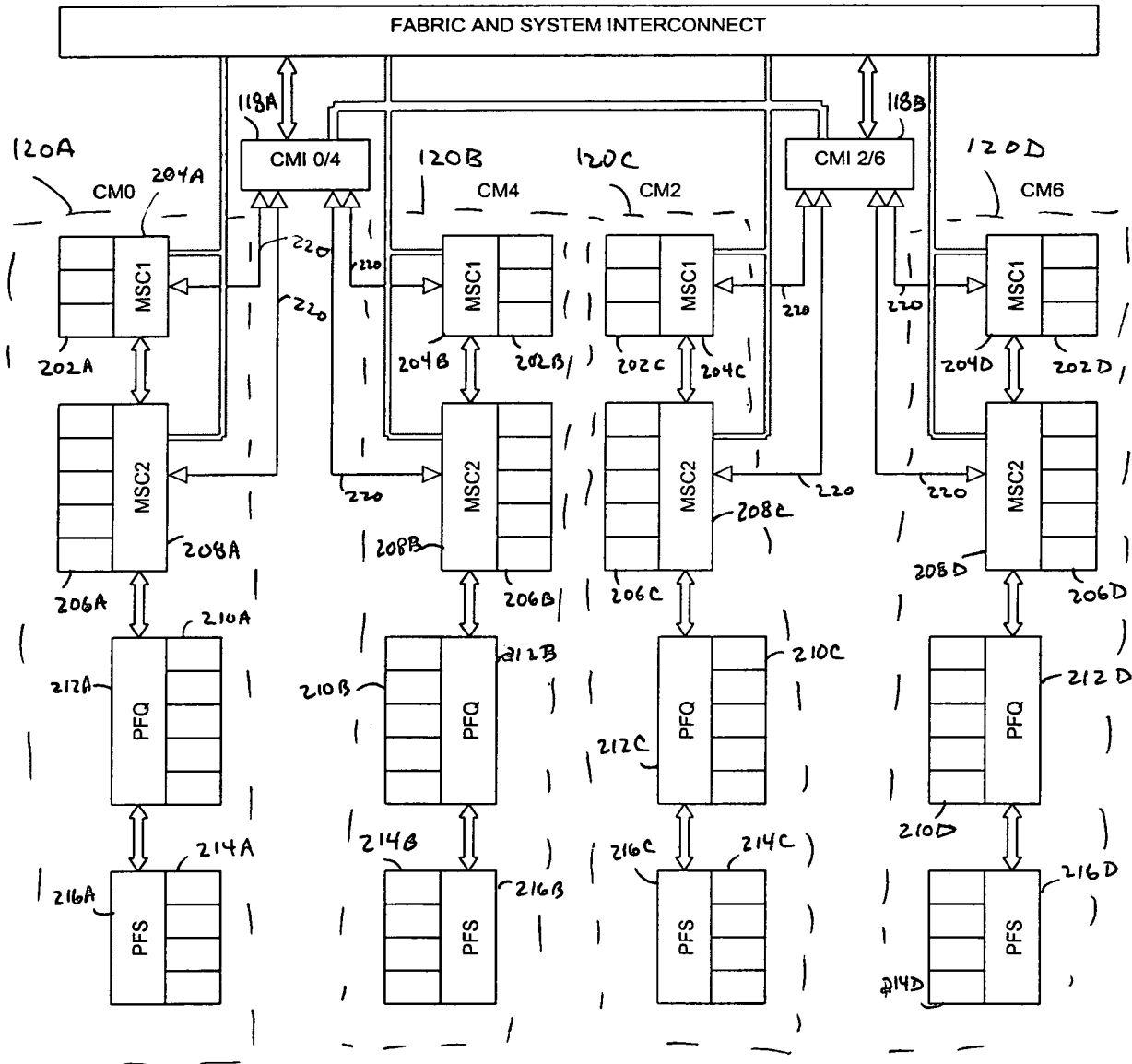


FIG. 6B

6024454-00260

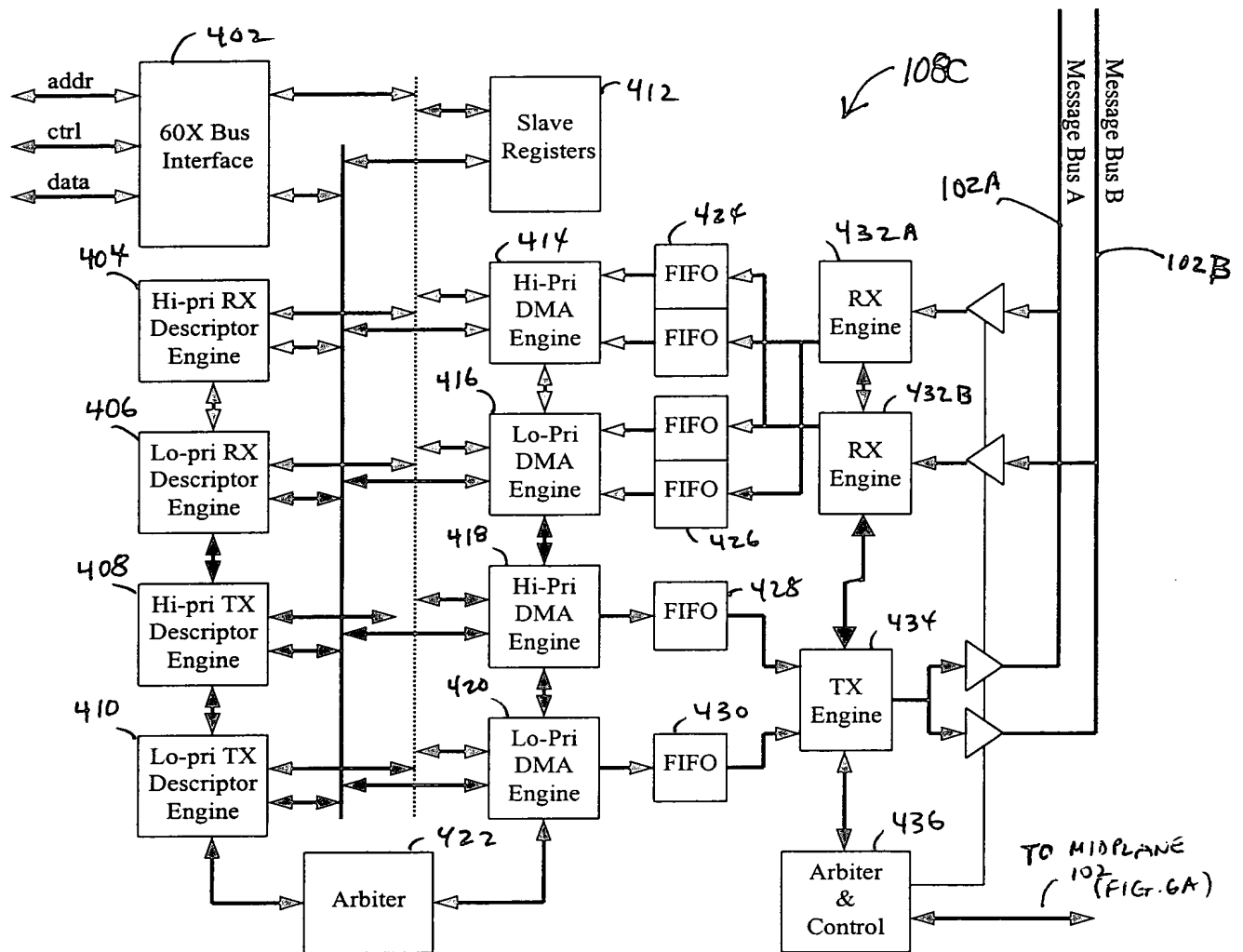


FIG. 6C

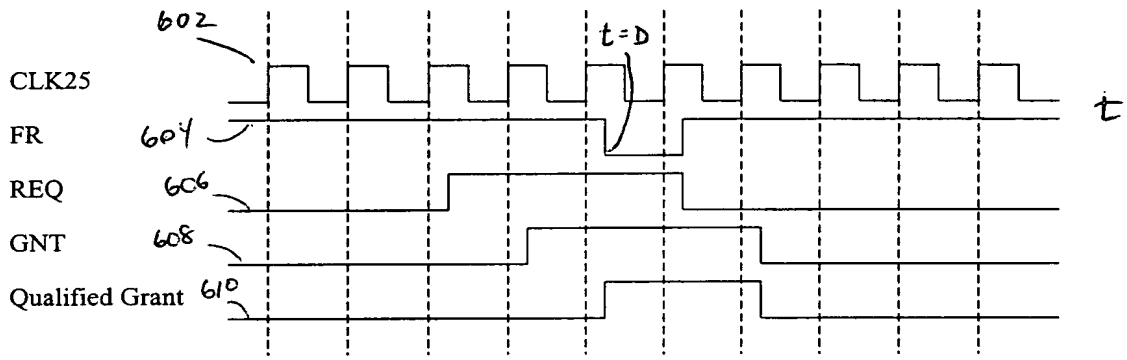


FIG. 6E

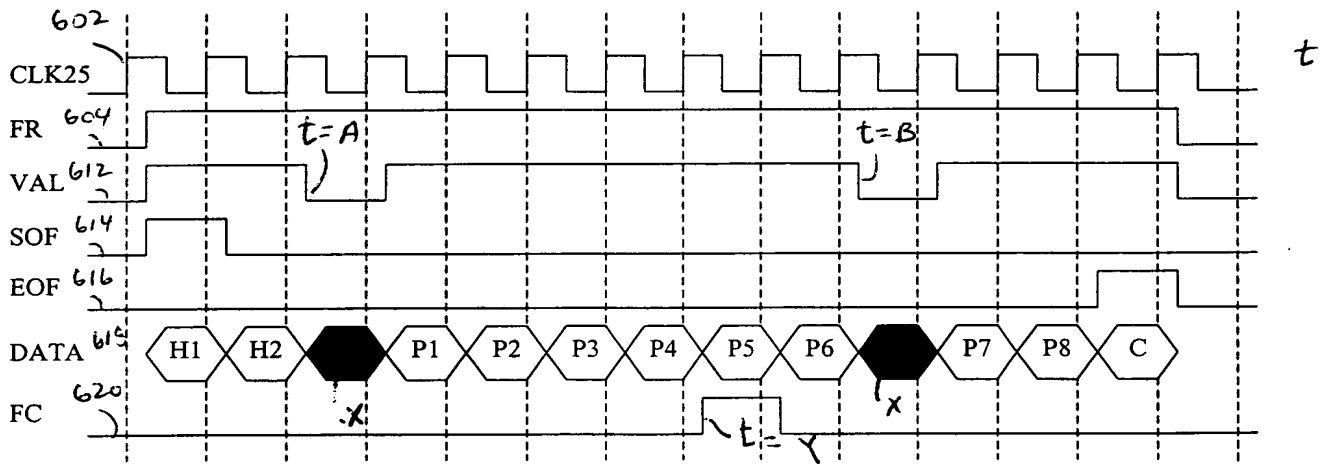


FIG. 6F

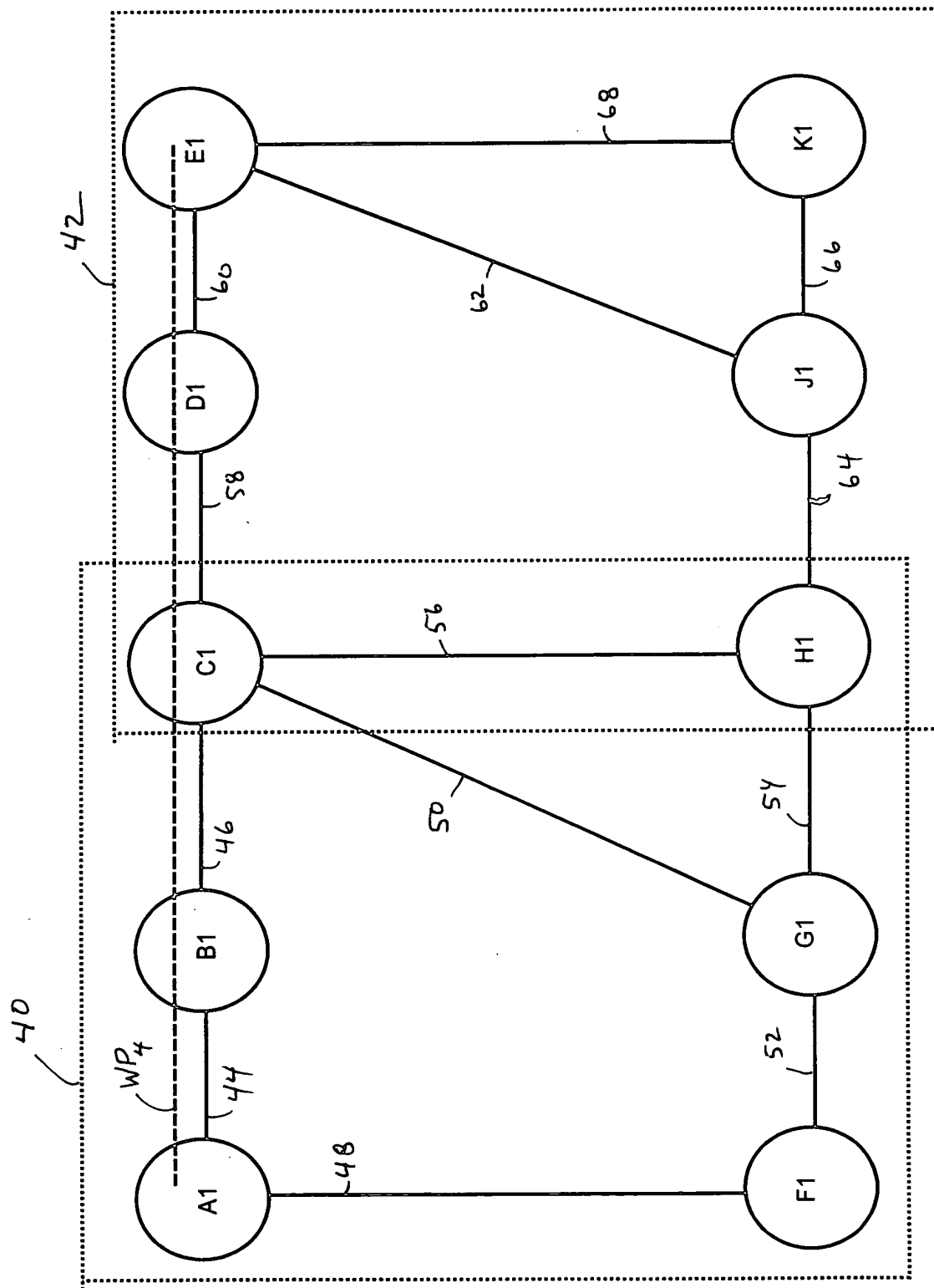


FIG. 7

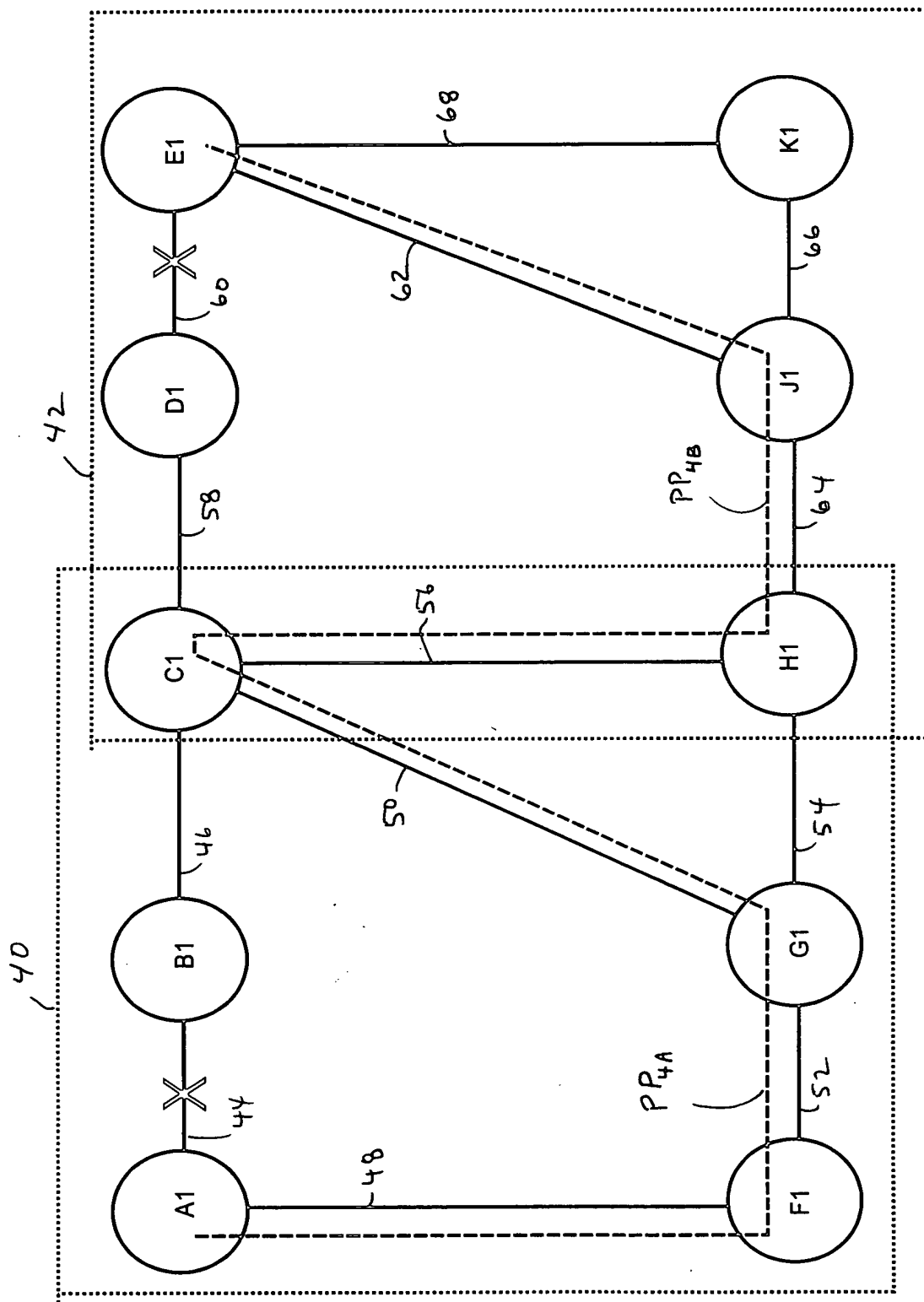


FIG. 8

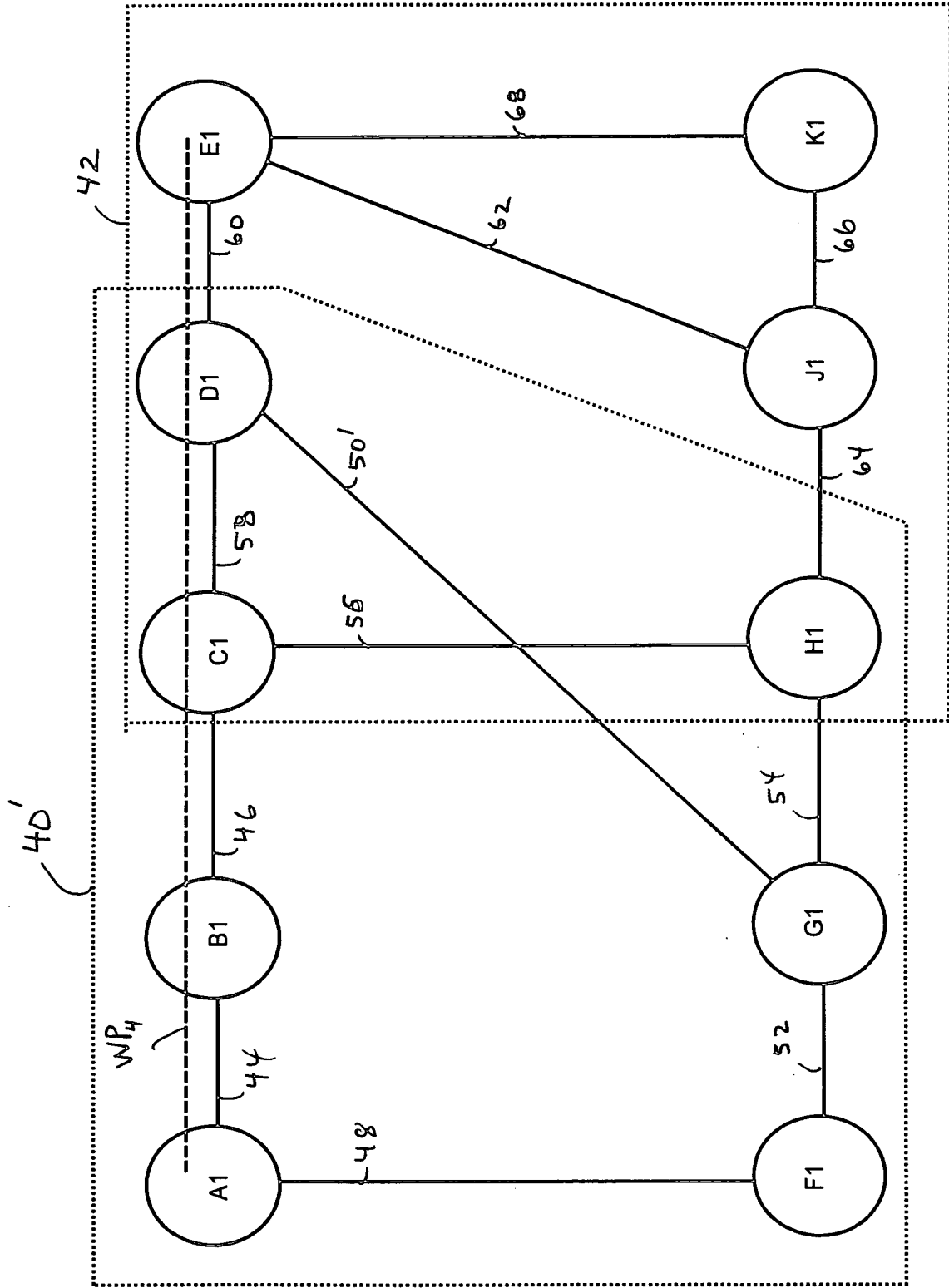


FIG. 9

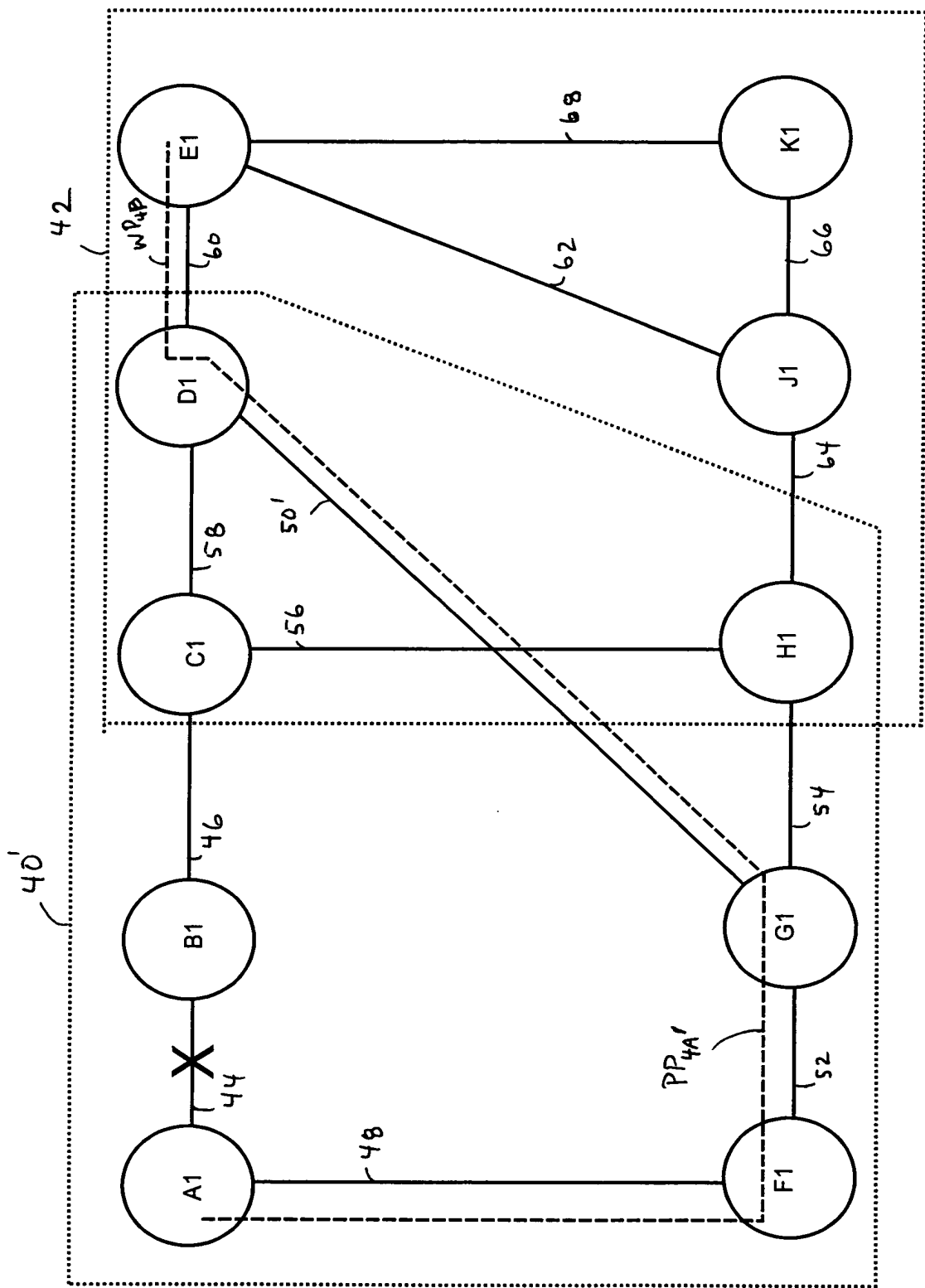


FIG. 10

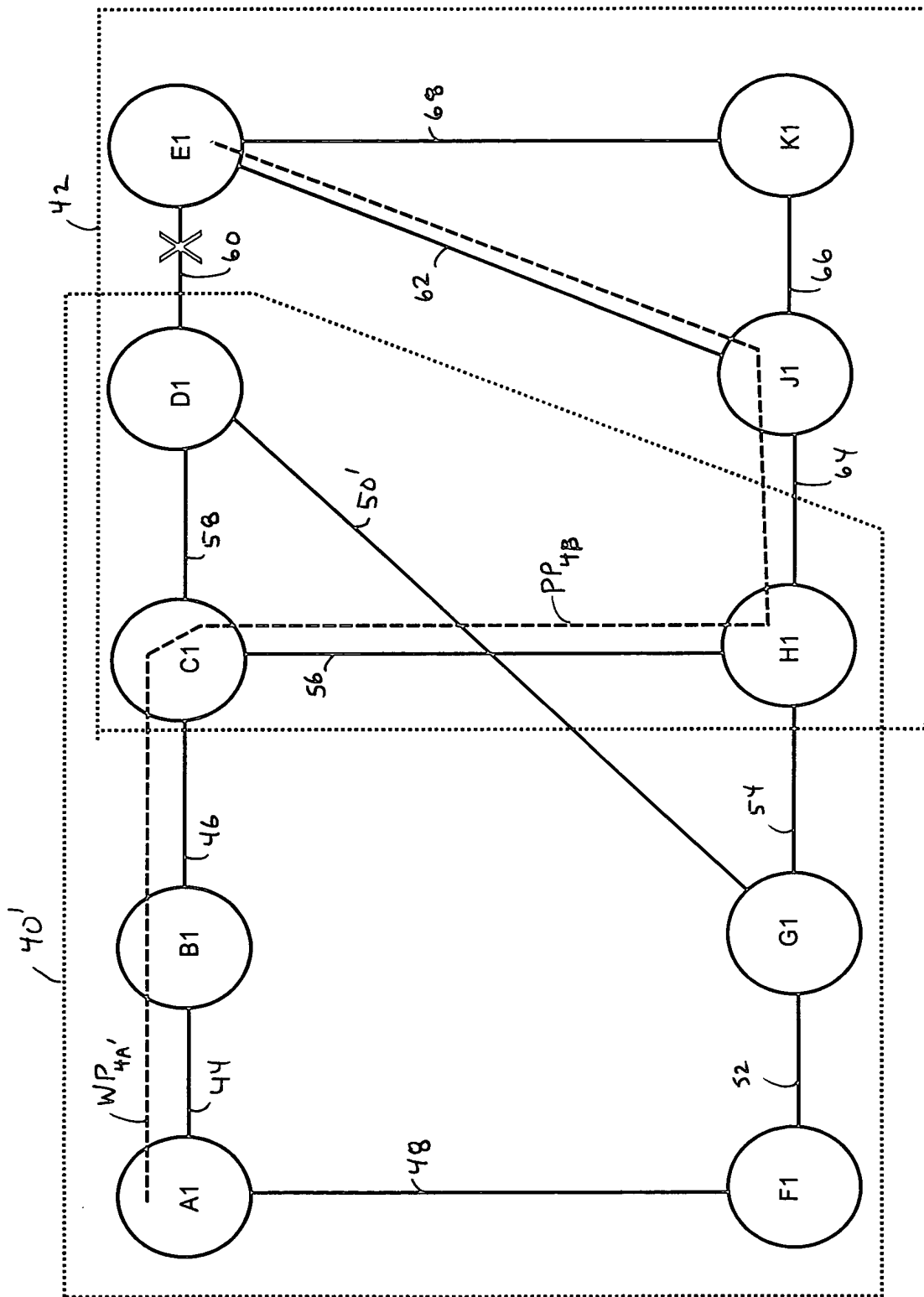


FIG. 11

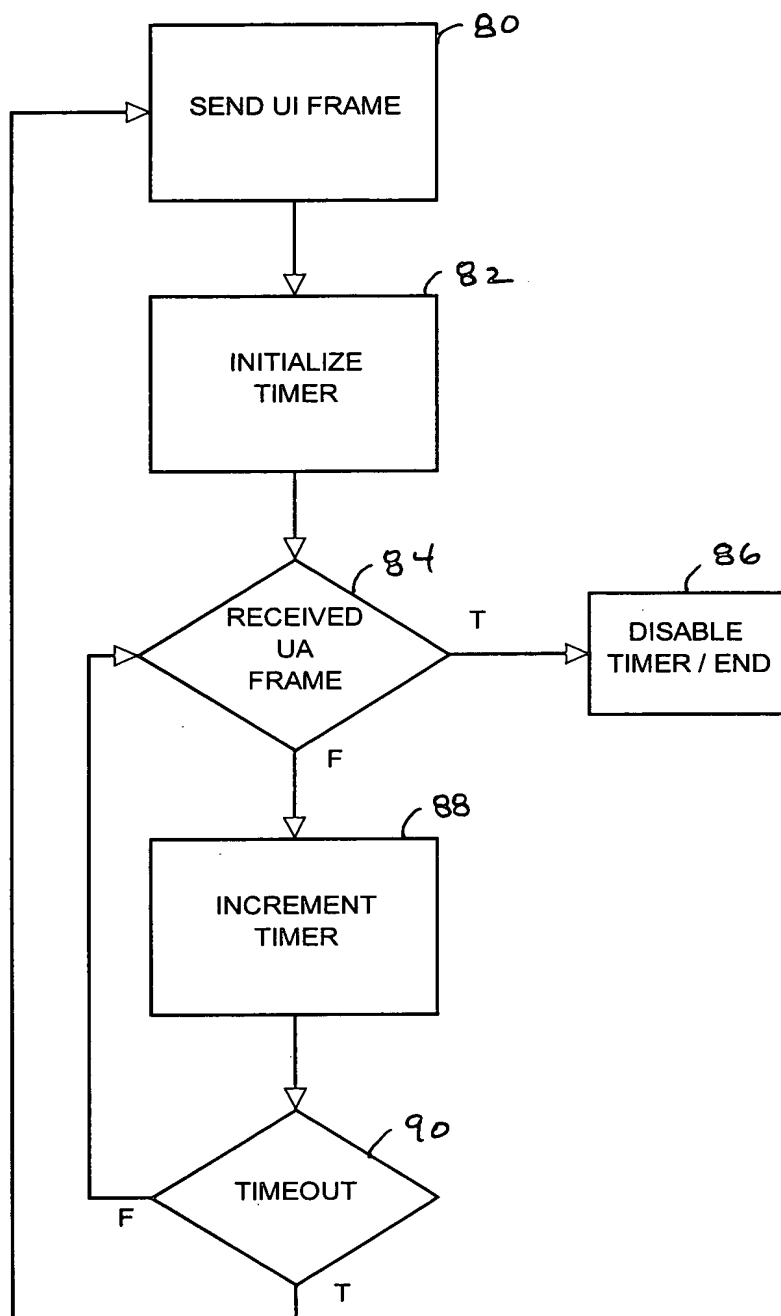


FIG. 12

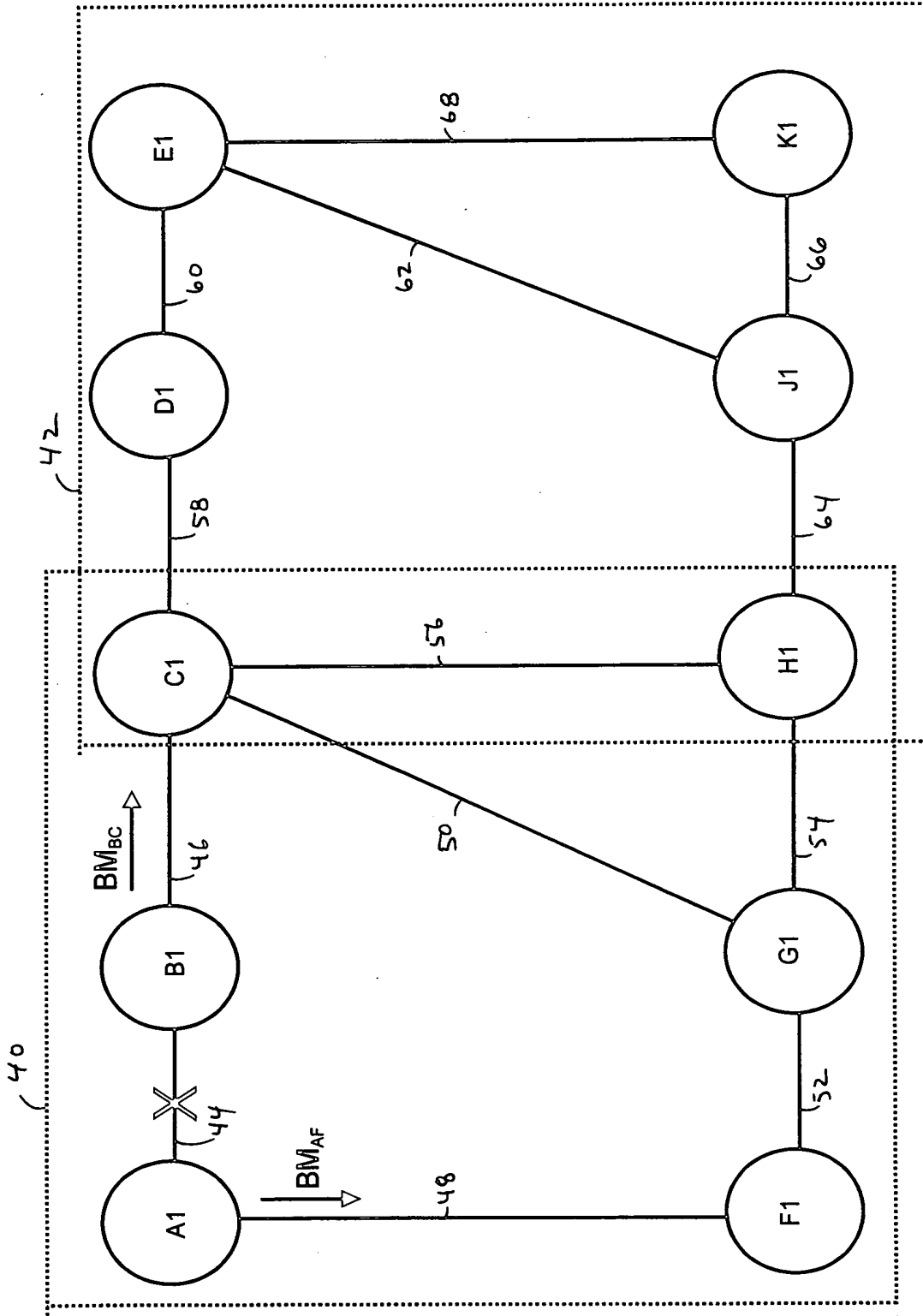


FIG. 13A

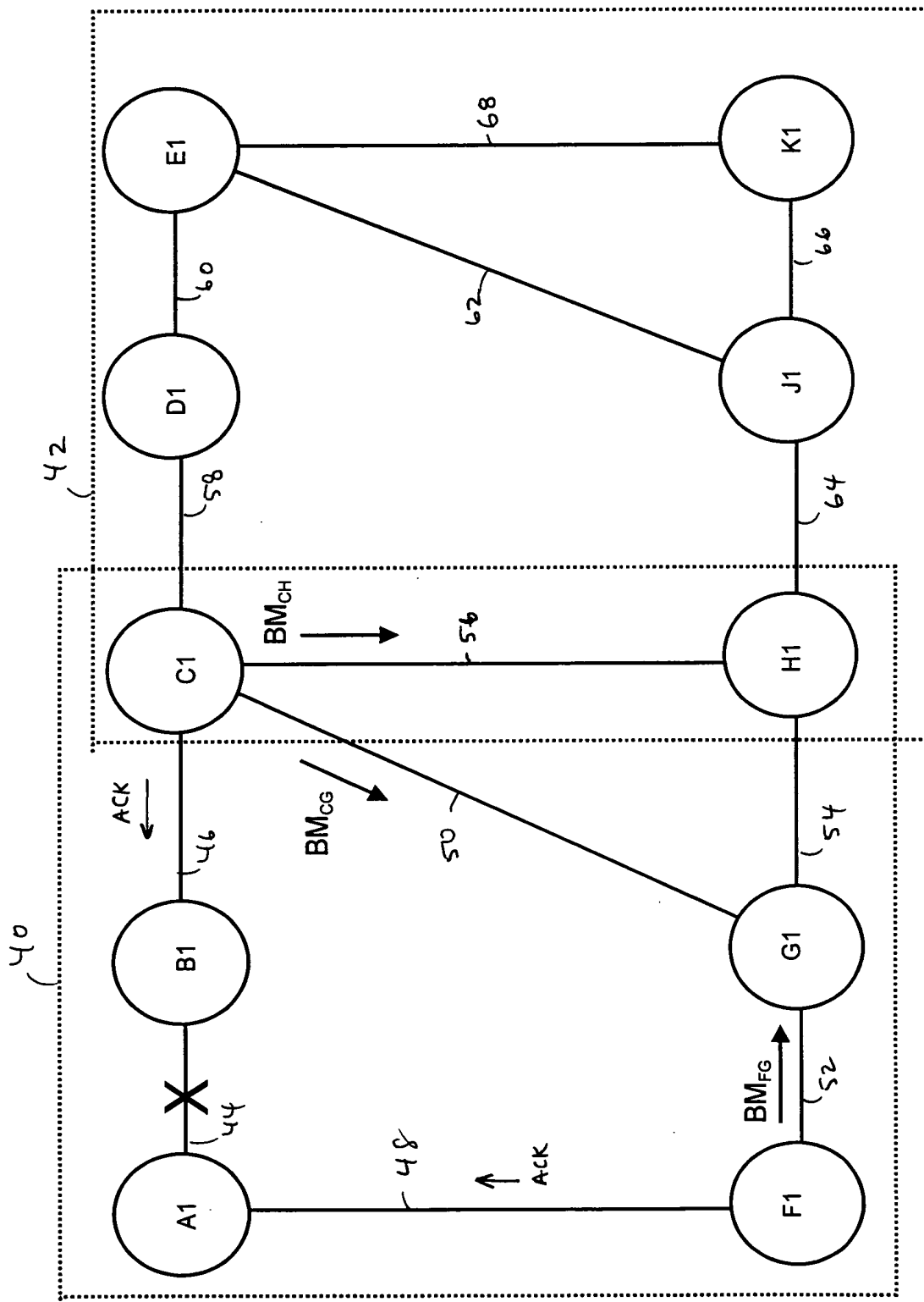


FIG. 13B

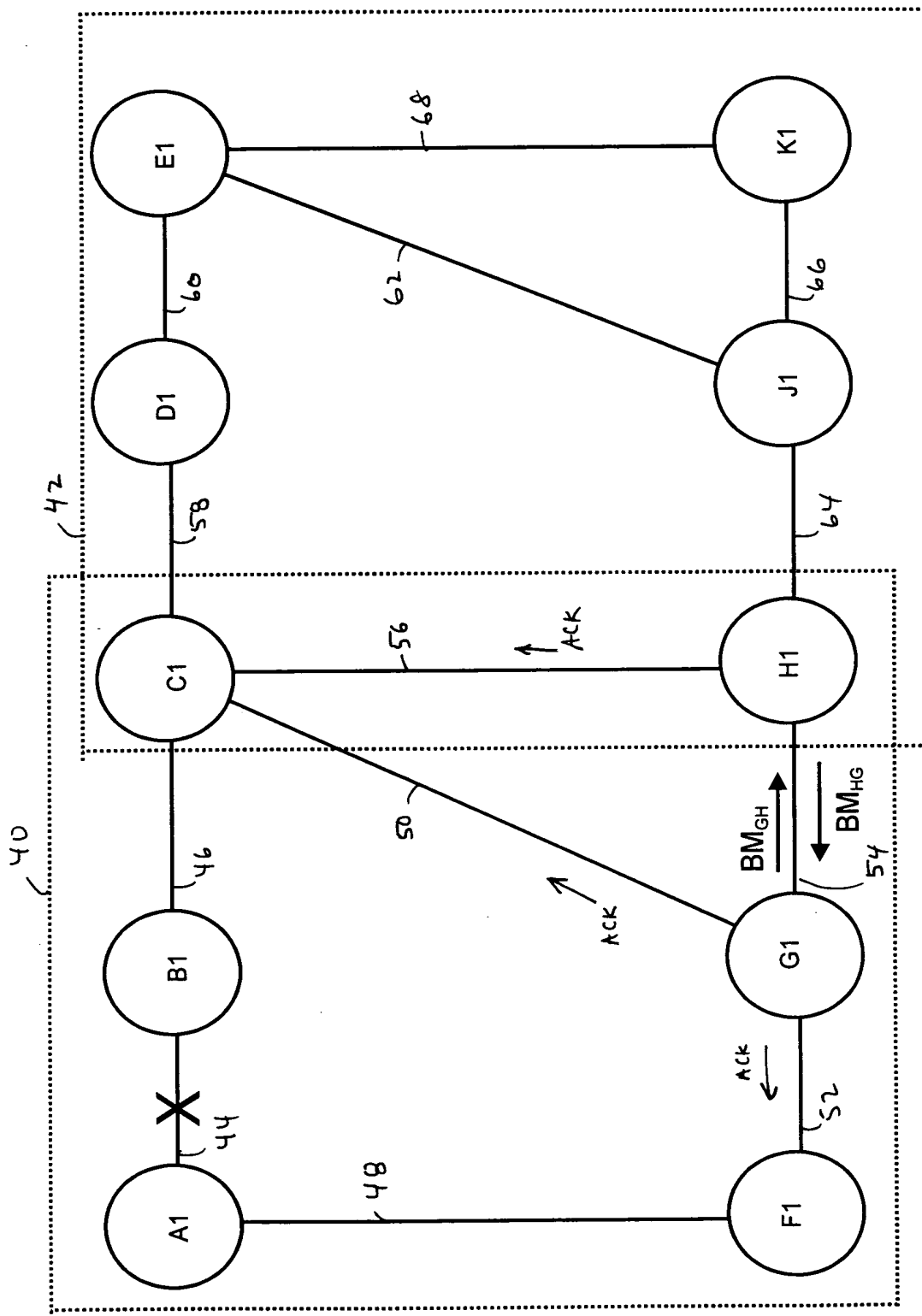
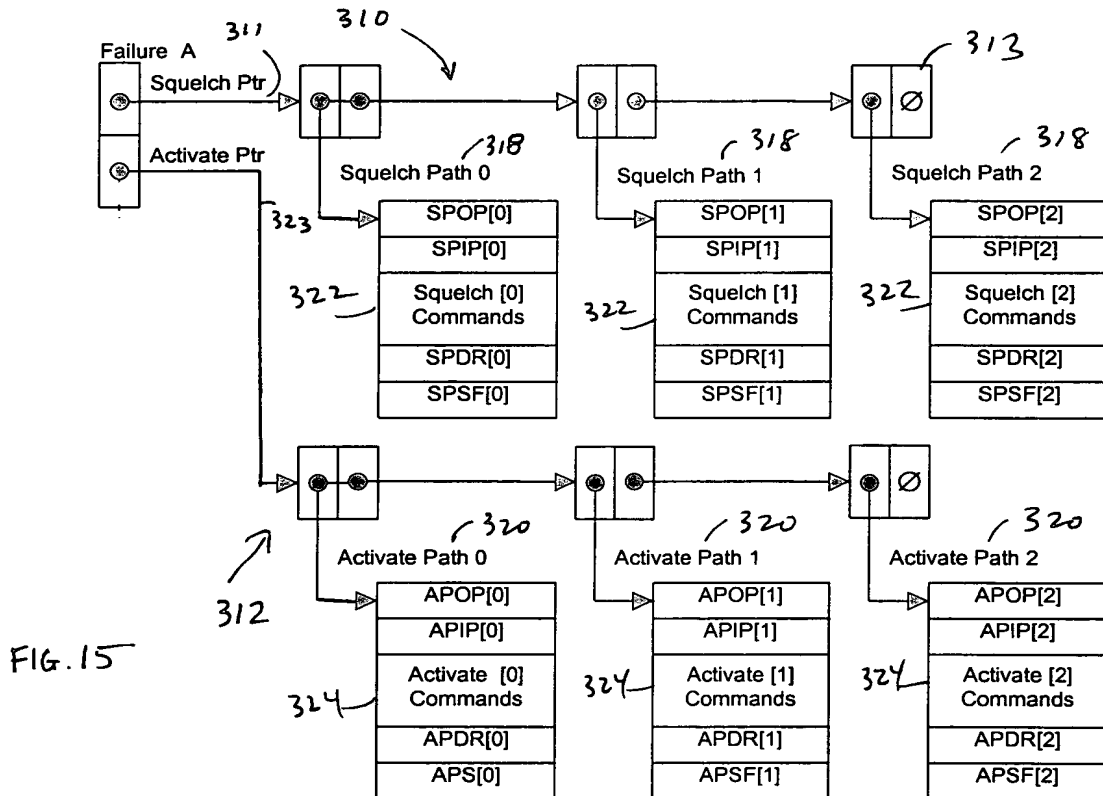
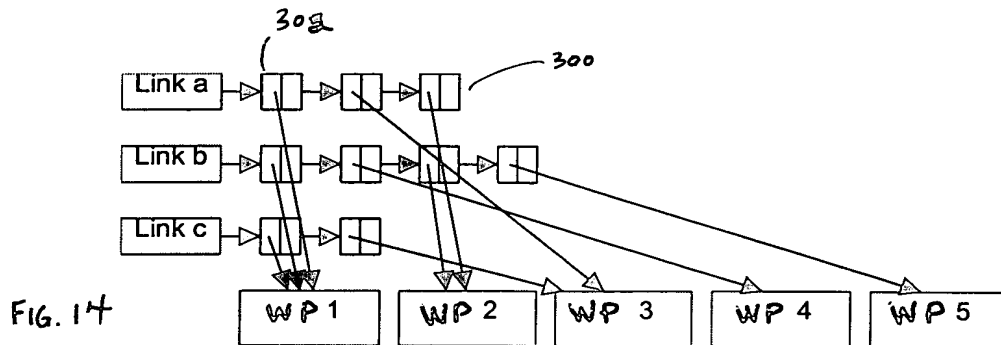


Fig. 13C



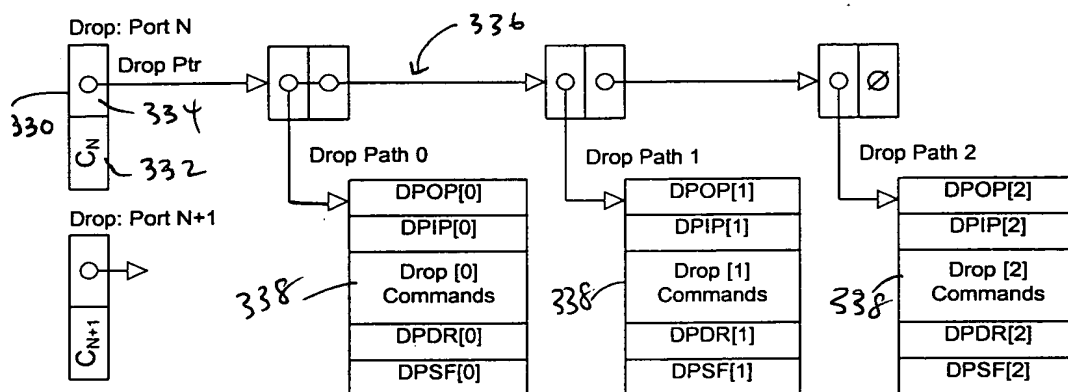


FIG. 16

0x000	spare [15:0]	Port 0 Output Capacity [15:0]
0x004	Port 0 Drop Pointer [21:0]	
0x008	spare [15:0]	Port 1 Output Capacity [15:0]
0x00C	Port 1 Drop Pointer [21:0]	
0x010	spare [15:0]	Port 2 Output Capacity [15:0]
0x014	Port 2 Drop Pointer [21:0]	
0x018	spare [15:0]	Port 3 Output Capacity [15:0]
0x01C	Port 3 Drop Pointer [21:0]	
⋮	⋮	
0x1E0	spare [15:0]	Port 124 Output Capacity [15:0]
0x1E4	Port 124 Drop Pointer [21:0]	
0x1E8	spare [15:0]	Port 125 Output Capacity [15:0]
0x1EC	Port 125 Drop Pointer [21:0]	
0x1F0	spare [15:0]	Port 126 Output Capacity [15:0]
0x1F4	Port 126 Drop Pointer [21:0]	
0x1F8	spare [15:0]	Port 127 Output Capacity [15:0]
0x1FC	Port 127 Drop Pointer [21:0]	

FIG. 17